

Class	Subclass	ISSUE CLASSIFICATION
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**PATENT NUMBER**

## U.S. UTILITY Patent Application

O.I.P.E.	PATENT DATE
SCANNED 172 0.1 2022	

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/918739	D	438	666	2827 <del>2874</del>	D. GRAYBILL

David Corleis

Leads under chip in conventional IC package

PTO-2040  
12/99

[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b> Sheets Drawn      Figs. Drawn      Print Fig.		<b>CLAIMS ALLOWED</b> Total Claims      Print Claim for O.G.	
	<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. _____ (Assistant Examiner)      (Date)		<b>NOTICE OF ALLOWANCE MAILED</b> _____ _____ <b>ISSUE FEE</b> Amount Due      Date Paid	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____ _____ _____	_____ (Primary Examiner)      (Date)		<b>ISSUE BATCH NUMBER</b> _____ (Legal Instruments Examiner)      (Date)	
	<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.			

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